

## REMARKS

Claims 21 - 37 are pending; original claims 1 – 20 are canceled.

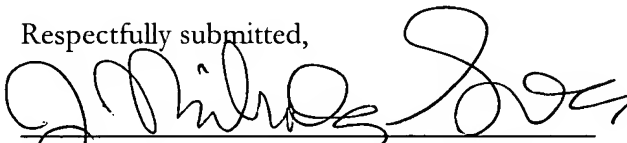
The present claims are generally directed to a species of claims identified in a restriction requirement dated July 7, 2003 in parent application serial no. 10/298,916 (Species V).

The present amendment to the specification is also concerned with expressly withdrawing the claim to priority made in the original application papers to certain unrelated cases filed earlier by the present applicant, and amending the specification and claims to better clarify the scope of the inventions presented in the present filing. No new matter is presented.

Accordingly, the Examiner is requested to take note that the Applicants are claiming priority to the parent and grandparent applications (serial no. 10/298,916 and 10/028,085 respectively) and not claiming any relationship or priority to the following applications under 35 USC § 120: Serial no. 09/603,101 entitled “A CMOS-PROCESS COMPATIBLE, TUNABLE NDR (NEGATIVE DIFFERENTIAL RESISTANCE) DEVICE AND METHOD OF OPERATING SAME”; Serial No. 09/603,102 entitled “CHARGE TRAPPING DEVICE AND METHOD FOR IMPLEMENTING A TRANSISTOR HAVING A NEGATIVE DIFFERENTIAL RESISTANCE MODE”; Serial No. 09/602,658 entitled “CMOS COMPATIBLE PROCESS FOR MAKING A TUNABLE NEGATIVE DIFFERENTIAL RESISTANCE (NDR) DEVICE. All of the above were filed June 22, 2000, and while they are incorporated by reference for purposes of ensuring a complete disclosure, they should be considered as prior art against the present application. This action is being taken to ensure that the inventions presented in the present application to the disclosed subject matter are considered distinctly and independently from the above applications, and in light of any potential prior art that may exist.

A transmittal sheet and fee are also enclosed. Please charge any additional fees due to deposit account no. 501 – 244.

Respectfully submitted,

  
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